

Solution of AC/DC Power Flow on a Multi-Terminal HVDC System: Illustrative Case Supergrid Phase I

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Abstract— This paper presents an algorithm for the sequential solution of the ac/dc power flow, which is proposed for the analysis of multi-terminal HVDC systems (MTDC). This sequential power flow algorithm can be implemented easily in an existing ac power flow package and is very flexible when it compared with unified methods. Gauss-Seidel algorithm is used to solve dc power balance equations, it offers two keys advantages: very fast and simple computational implementation, and errors do not accumulate during the calculation. The algorithm is tested using the WSCC 3-machine, 9-bus system with a 3-terminal MTDC network and results compared with those obtained from DIGSILENT[®] PowerFactory[™] demonstrating the validity of the proposed algorithm. As aggregate value, a representative test case of the projected scheme for the phase I of the Supergrid project on the North Sea is presented, the proposed approach presented in this paper is used to calculate DC power flows for some scenarios.

Index Terms— High voltage direct current, Multi-terminal HVDC, MTDC, Power flow, Voltage Source Converters.

I. INTRODUCTION

There are several challenges that de future power systems will face in coming future. One of them is meets rising energy requirements in a manner that is: *sustainable, secure, and competitive*. There is no single answer for this situation, however, there are several aspects to consider regarding primary resource [1], [2]: (i) greater energy efficiency and conservation, (ii) increased use of resources that are secure, indigenous, sustainable, *clean* and competitive.

A potentially realistic solution for this situation is the use of a primary energy source which is a clean and fuel cost-free, wind power. This resource is enormous in the Europe's offshore and the amount available is able to meet Europe's demand seven times over. There are 150 GW of offshore wind projects are already in various stages of planning [2], [3]. The North Sea has a vast potential for renewable energy generation: offshore wind power, tidal and wave energy.

High Voltage Direct Current (HVDC) systems are more flexible than their ac counterparts and it offers distinct advantages for the integrating offshore wind farms to inland grid system. The *Voltage Source Converter* (VSC) HVDC transmission system enable fast and flexible control active and reactive power, and can alleviate the propagation of

voltage and frequency deviations due to wind variations in wind strength. It seems advances on technologies open the door for VSC HVDC systems at higher voltage and higher power range, which is making *multi-terminal HVDC* (MTDC) system a technical possibility [4], [5], [6], [7], [8].

The solution methods for HVDC power flow are generally divided in *sequential* and *unified methods*. The unified (simultaneous) method was originally suggested by Arrillaga [9] and co-workers, the ac and dc system are solved together [10], [11], the dc equations along with the power flow equations and solves the combined set simultaneously. The sequential method was proposed by Reeve et al [12], it solve the dc system equations using interface variables as computed from ac power flow [13], [14]. Sequential approach is quite easy to developed and integrated into existing ac based power flow software while unified approach a whole implementation is needed.

Few publications have been developed in the present time for MTDC, *Temesgen et al* [15], presents a numerical iteration based upon Newton-Raphson approximation for lossless converter stations using the unified approach. *Beerten et al* [16], [17] have used the sequential approach for the MTDC power flow problem, they have included converter losses and defining the power set-points with respect to the system bus. In [18] the concept of distributed dc voltage control for power flow is included.

This paper presents a general algorithm for VSC-MTDC power flow calculations based on the Gauss-Seidel approach. The proposed algorithm is used for the dc network and does not impose any restriction on the topology configuration (more than two terminals) or configuration of the dc network.

The algorithm is implemented and integrated into an existing ac power flow package and it is tested over a test network. Section II shows the algorithm for MTDC network power flow analysis. Section III presents simulation and results over WSCC 3-machine, 9-bus system [19] with a 3-terminal MTDC network using proposed approach which is implemented using MATLAB[®] and integrated into ac power flow program. Results obtained with the proposed algorithm are compared with those obtained using DIGSILENT[®] PowerFactory[™] v14.0.525.1 [20], it demonstrated the validity of the proposed algorithm. Section IV presents a test

case based on a representative and realistic scheme for the phase I of the project Supergrid on the North Sea, the proposed approach presented in this paper is used to calculate dc power flows. Finally, conclusions of this paper are presented.

II. POWER FLOW ON MTDC NETWORKS

A classical problem of circuit theory is to find all branch currents and all node voltages of an assigned circuit. In general, the power flow problem consists in finding the zero of a set of nonlinear equations starting from an adequate initial guess.

A. AC Power Flow Problem

The most general form of the power flow equations is based on the special case of the set of *differential-algebraic-equations* (DAE) on steady-state [21]. Under that circumstances the power flow equations is reduced to a set of nonlinear set of the algebraic equations [2]:

$$\mathbf{g}(\mathbf{x}) = \mathbf{0} \quad (1)$$

where \mathbf{g} is the set of algebraic equations define the power balance at network buses.

The classical formulation of ac power flow equations for a n -node network, defines the nodal injected current vector ($\mathbf{I}=[I_1, I_2, I_n]^T$) as function of voltage vector ($\mathbf{V}=[V_1, V_2, V_n]^T$) and admittance matrix ($\mathbf{Y}=\{Y_{ij}\}$):

$$\mathbf{I} = \mathbf{YV} \quad (2)$$

$$I_i = \sum_{j=1}^n Y_{i,j} V_j \quad i = 1, 2, \dots, n-1 \quad (3)$$

which leads to write the complex power injections (\mathbf{S}) at nodes as:

$$\mathbf{S} = \mathbf{VI}^* = \mathbf{VY}^* \mathbf{V}^* \quad (4)$$

In the classical power flow formulation, the variables are voltage amplitudes ($|V_i|$) and phases (δ_i) at load node, reactive powers ($Q_{ac,i}$) and voltage phases at generator PV node and active ($P_{ac,i}$) and reactive power at the slack node.

$$P_{ac,i} = \sum_{j=1}^n |V_i| |V_j| |Y_{ij}| \cos(\delta_i - \delta_j - \theta_{ij}) \quad (5)$$

$$Q_{ac,i} = \sum_{j=1}^n |V_i| |V_j| |Y_{ij}| \sin(\delta_i - \delta_j - \theta_{ij}) \quad (6)$$

A complete explanation for the classical ac power flow can be found on [22], [23], [21], [24].

B. DC Power Flow Problem

Consider a dc network which consists of n_{dc} dc nodes networks (see Fig. 1), each node is characterized by nodal voltage ($U_{dc,i}$), and nodal ($P_{dc,i}$) power injected into the dc network. The current injected at the i -th dc node ($I_{dc,i}$) can be written as [2]:

$$I_{dc,i} = \sum_{\substack{j=1 \\ j \neq i}}^{n_{dc}} Y_{dc,i,j} (U_{dc,i} - U_{dc,j}) \quad i = 1, 2, \dots, n_{dc}-1 \quad (7)$$

Combining the current equations into a matrix form:

$$\mathbf{I}_{dc} = \mathbf{Y}_{dc} \mathbf{U}_{dc} \quad (8)$$

where the dc current vector $\mathbf{I}_{dc} = [I_{dc,1}, I_{dc,2}, \dots, I_{dc,n_{dc}}]$, $\mathbf{V}_{dc} = [U_{dc,1}, U_{dc,2}, \dots, U_{dc,n_{dc}}]$ is the dc voltage vector and

$\mathbf{Y}_{dc} = \{Y_{dc,i,j}\}$ is the dc bus admittance matrix. The current injections \mathbf{I}_{dc} are not known prior to the power flow solution for the dc network.

For a bipolar dc network, the active power injected i -th node ($P_{dc,i}$) can be written as [2]:

$$P_{dc,i} = 2U_{dc,i} I_{dc,i} \quad (9)$$

C. AC/DC Power Flow Problem

Fig. 1 shows a general representation of a MTDC system. This system consists of n_{dc} dc nodes which are connected to the ac system using VSC converter stations.

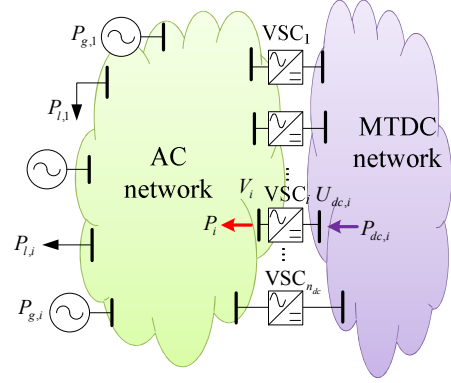


Fig. 1. Representative scheme of MTDC system connected to ac power system.

Power losses at each converter stations are neglected for simplicity. Fig 2 show a representation of loss-less VSC HVDC converter station used on MTDC system, the main variables are depicted and references for power flow are assumed on such directions.

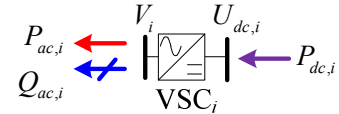


Fig. 2. Representative scheme for a VSC HVDC converter station.

The active power ($P_{ac,i}$) conservation between the ac and dc side can be written as:

$$P_{ac,i} = \text{Re}(V_{ac,i} I_{ac,i}^*) = U_{dc,i} I_{dc,i} = P_{dc,i} \quad (10)$$

For no-over modulated loss-less VSC converter ($P_m < 1$), the relation between ac and dc voltages can be written as:

$$\begin{aligned} V_{acr,i} &= \text{Re}(V_{ac,i}) = K_0 P_{mR} U_{dc,i} \\ V_{aci,i} &= \text{Im}(V_{ac,i}) = K_0 P_{mI} U_{dc,i} \end{aligned} \quad (11)$$

where V_{acr} is the real part of ac voltage, V_{aci} is the imaginary part of ac voltage, K_0 is the constant depending on the modulation method, P_{mR} is the real part of modulation index and P_{mI} is the imaginary part of modulation index.

D. Proposed Solution of AC/DC Power Flow

The solution of the power flow problem considering the ac/dc system requires a special approach.

In this paper, the power flow on the dc network as well as the power flow at the ac network each has to be solved iteratively in the *sequential approach*.

In order to include the VSC-HVDC stations into the ac power flow equations, some considerations from power flow point-of-view is taken in account. The operation mode of the converter station defines those considerations. Two reactive

power control functions are included into VSC-HVDC stations from the ac network side [2]:

(i) *Q-mode*: the reactive power injected ($Q_{ac,i}$) into the ac network is kept constant, as consequence the ac voltage (V_i) might change.

(ii) *V-mode*: the reactive power converter injection ($Q_{ac,i}$) is enough to keep its ac node voltage magnitude (V_i) constant.

On the dc network side, there are two different control functions for each converter:

(i) *P_{ac}-control*: The active power ($P_{ac,i}$) injected in the ac network is kept constant and the ac voltage (V_i) is allowed to vary. This situation can be modelled as constant negative load (PQ-node).

(ii) *U_{dc}-control*: The converter controls its active power injection ($P_{ac,i}$) to keep its dc node voltage constant ($U_{dc,i}$). This situation is modelled as a voltage controlled source (PV-node).

All but one converter works on *P_{ac}-mode*, controlling the active power injection into the ac network; one converter station must have its controller working as *U_{dc}-control* and it is named *dc-slack converter*. The actual value of the active power injection of this converter is not known prior the power flow solution. As the ac power flow, the dc slack converter is the node that covers the dc network losses.

The dc voltage at this node ($U_{dc,i}$), assuming a loss-less converter station ($P_{ac,i} = P_{dc,i}$), can be calculated from:

$$P_{dc,i} = 2U_{dc,i} \sum_{\substack{j=1 \\ j \neq i}}^{n_{dc}} Y_{dci,j} (U_{dc,i} - U_{dc,j}) \quad (12)$$

The nonlinear nature in terms of voltage node ($U_{dc,i}$) of this problem is evident from (12). Numerical methods are employed to obtain a solution that is within an acceptable tolerance.

A suitable method to solve (12) is the *Gauss-Seidel (GS) method*, also known as the *Liebmann method*, under this approach. The GS algorithm is applied to the power flow equations of the DC network. The following steps describe this procedure:

Step 0: Formulate and assemble $n_{dc} \times n_{dc}$ dc admittance matrix Y_{DC} .

Step 1: Assign initial guesses to $(n_{dc}-1)$ unknown dc node voltage:

$$U_{dc,i}^{(k+1)} = 1.00 \text{ p.u.}, \quad i = 1, 2, 3, \dots, (n_{dc}-1) \quad (13)$$

The dc slack node is assumed as n_{dc} th node.

Step 2a: For the *P-control* VSC converter node, find $U_{dc,i}$

$$U_{dc,i}^{(k+1)} = \frac{1}{Y_{dci,i}} \left[\frac{0.5P_{dc,i}}{U_{dc,i}^{(k)}} - \sum_{\substack{j=1 \\ j \neq i}}^{n_{dc}} Y_{dci,j} U_{dc,j}^{(k)} \right] \quad (14)$$

where k =iteration number.

Step 2b: For faster convergence, apply acceleration factor (α) to *P-Control* VSC converter node:

$$U_{dc,i,ACEL}^{(k+1)} = U_{dc,i,ACEL}^{(k)} + \alpha (U_{dc,i}^{(k)} - U_{dc,i,ACEL}^{(k)})$$

where subscript *ACEL* means the accelerated value.

Step 3: Check convergence. That is, the value of the difference of the node voltage between successive iterations should be less than a tolerance value (ϵ).

Step 4: Find dc slack node power:

$$P_{dc,i} = 2U_{dc,n_{dc}} \sum_{j=1}^{n_{dc}-1} Y_{dci,j} (U_{dc,n_{dc}} - U_{dc,j}) \quad i = n_{dc} \quad (15)$$

The algorithm described above is now combined in a sequential ac/dc power flow algorithm that is depicted on Fig. 3.

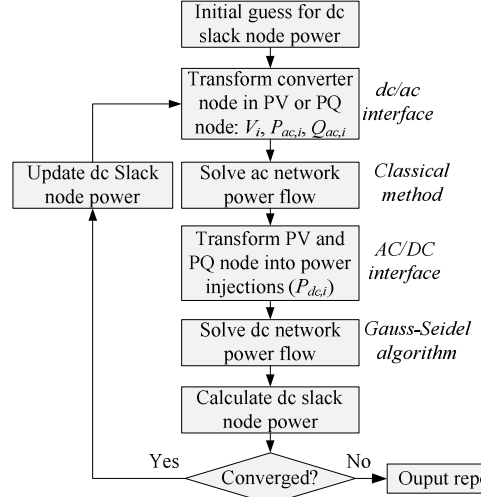
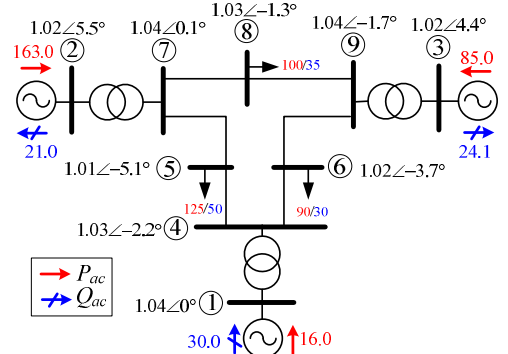


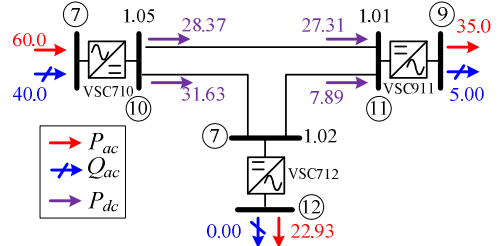
Fig. 3. Flowchart of sequential power flow for AC/DC power flow algorithm.

III. SIMULATION AND RESULTS

In order to demonstrate the effectiveness of the proposed AC/DC power flow approach, a MATLAB® [25] version 7.12.0.635 (R2011a 64-bit) program (m-file) is developed for this purpose. This algorithm is integrated with the AC power flow program *Power System Toolbox (PST)* [26], an open-source MATLAB® toolbox.



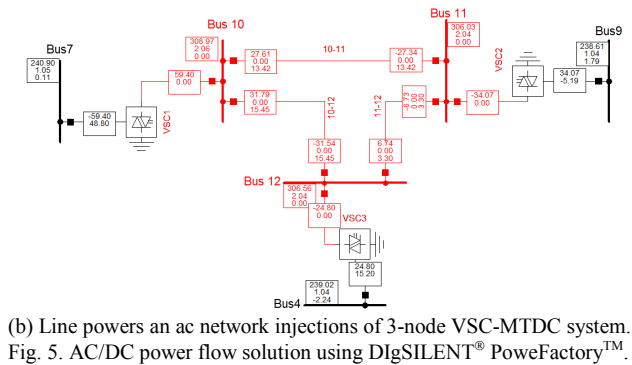
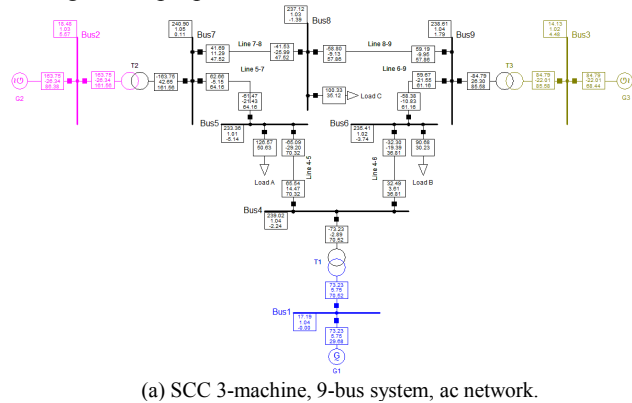
(a) WSCC 3-machine 9-bus system, ac network.



(b) Line powers and ac network injections of 3-node VSC-MTDC system. Fig. 4. AC/DC power flow solution using the proposed approach.

Simulation is carried out on WSCC 3-machine, 9-bus system [19] with a 3-terminal MTDC network presented on [17]. The MTDC network is connected between node 4, 7, and 9. The converter station at node 4 is defined as dc slack (Q_{ac} -mode: U_{dc} -control) to keep constant $U_{dc,3} = 1.00$ p.u., whereas the other converter stations are used on P -control. Data of converter, 100MW 2x150kV, and line resistances were obtained from [17]. The ac/dc power flow results of the proposed approach in this paper are shown in Fig 4a y 4b.

Fig 5 shows the results calculated using DlgSILENT[®] PowerFactory[™] v14.0.525.1 [20]. Quantitative comparison in terms of voltages and power flow demonstrate the higher error is found on dc power flows; however, this is less than 0.987×10^{-6} p.u. This result demonstrates the effectiveness of the algorithm proposed.



IV. TEST OF INTERCONNECTION SCHEME

Supergrid will be the transmission backbone of Europe's decarbonised power sector. It will facilitate the trading of electricity across and it will strengthen security of supply [27]. Although the *Supergrid* has gotten much attention, it cannot be built yet. While the basic technology might seem available, several technical limitations still exist [28].

Many *Supergrid* topologies have been proposed or studied by different organizations [29], [30], [31]. Regulatory and policies aspects has been defined: A single planner (European Network of Transmission System Operators for Electricity, *Entso-e*), a single operator (ISO), a single grid code (*Entso-e*) and a single European regulator (ACER). However, North Sea *Supergrid* can probably not be built as a planned and optimally structure [32].

The main reason, independently planned projects would be

coupled together, leading to a rather grown network, comprising several dc and ac voltage levels and maybe different frequencies. The 2030 Possible optimized integrated offshore network development based on results of *Entso-e* is depicted on Fig. 5.

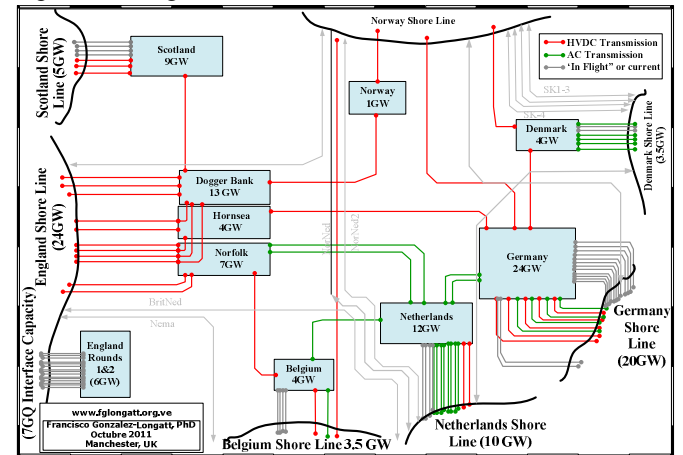


Fig. 5. 2030 Possible optimised integrated offshore network development.

It is based on the national target in terms of offshore wind power for North Sea national, scenario 2030, it was created by *European Wind Energy Association* (EWEA), and depicted on Fig. 6.

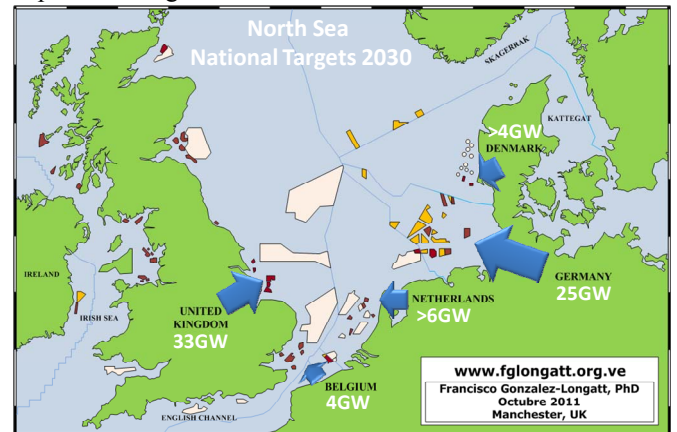


Fig. 6. North Sea National Target in terms of offshore wind power.

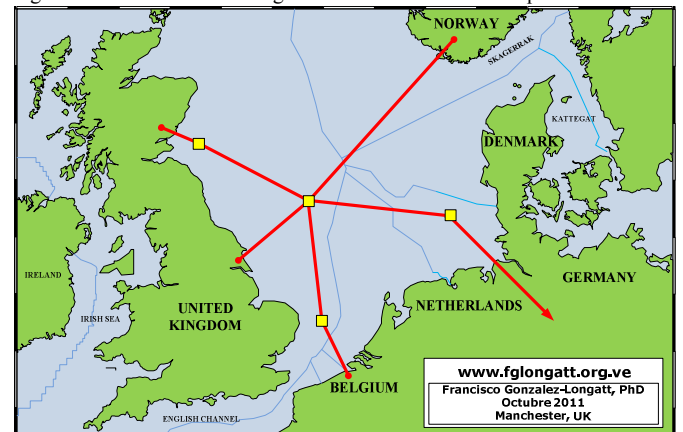


Fig. 7. *Supergrid* Phase I [33].

The *Supergrid* will be built out in phases, initially connecting the current crop of offshore wind generators to existing networks. As a first step, (Phase 1) nodes will be

built in the North Sea using 2015 technology to cluster offshore wind generation for bulk delivery. Fig. 7 shows a proposal scheme for Phase 1 of *Supergrid*

In this paper, the author introduced a *benchmark test system for the Phase I of Supergrid*, the proposal of such a test system is based on information publicly available on the scenario presented above for 2020-2025.

TABLE I.

INTERCONNECTION AND NODE CAPACITIES [33]

Connection	Capacity (GW)
Dogger-Germany Offshore	10.0
Dogger-Norfolk Bank	5.0
Dogger-Firth of Forth	5.0
Dogger-Norway	5.0
Germany Offshore-Munich	10.0
London-Norfolk Bank	5.0
Norfolk Bank-Belgium Offshore	2.0
<i>Nodes</i>	
Belgium Offshore	2.0
Dogger-Hornsea	10.0
Germany Offshore	10.0
Norfolk Bank	5.0
Munich	10.0
Firth of Forth	5.0

The approach proposed in this paper for ac/dc power flow is used on the benchmark test system (Fig. 8) to evaluate the steady state performance.

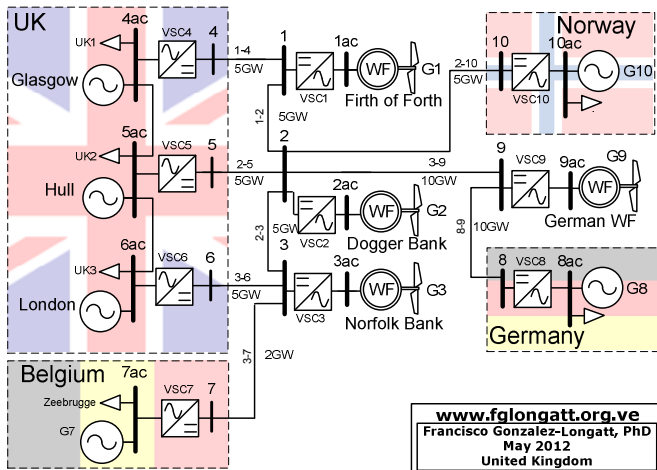


Fig. 8. Basic structure of the benchmark test system for Phase I of *Supergrid* Project.

In this system the energy from wind generation clusters off the east coast of the UK will be collected at *SuperNodes* at Firth of Forth, Dogger Bank/Hornsea and Norfolk Bank which will be connected together and interconnected with the German and Belgian North Sea clusters and Norwegian Hydro Power. The network then will deliver this power to the existing networks at terminals at Glasgow, Hull and Zeebrugge and nodes at London and Southern Germany (or North Rhine Westphalia).

Fig. 9 shows a summary ac/dc power flows expected for the different interconnections and nodes involves on the Phase I of *Supergrid*. This is high-generation scenario based on connecting 23,000 MW of offshore wind from the Firth-of-Forth, Dogger-Hornsea, Norfolk Bank, German and Belgian Offshore clusters and using technology expected to

be available between 2015 and 2020 [33].

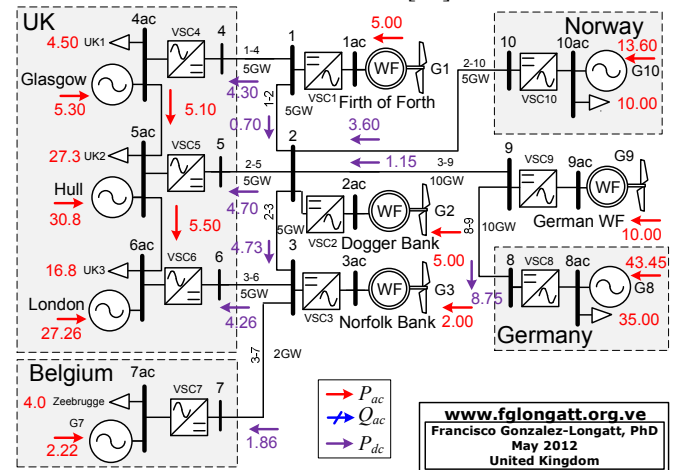


Fig. 9. AC/DC Power flow results for the benchmark test system for Phase I of *Supergrid* Project: High Generation Scenario

Scenario depicted on Fig. 9 shows how the MTDC offshore transmission network is used to link the hydro resources of Scandinavia with the marine and wind resources of Northern Europe. In this case, 3.6 GW is traded between England area and Norway area, based on the electricity market. The German wind farm contributes 10GW, 1.15 GW goes to the Supergrid market, and 8.89 GW will be injected in the Germany and North Europe power system. The scenario looks unrealistic a first glance (the highest amount of wind power production) however, results demonstrate technical feasibility of heavy power flow interchanges between parties involved. Power flow injection at converter substations and undersea cables are kept below rated power.

Results show an interesting technical and economic problem in terms of power losses on dc grid and converter station. The concept of unique slack bus on multi-terminal dc system creates a dilemma, which one will be dc node responsible for total power losses? This is a non trivial question and there is not straight-forward answer. There are several aspects to be considered, beyond the scope of this paper: the contracted transmission capacities, line limits and the power balance between the multiple synchronous grids are connected.

V. CONCLUSIONS

This paper presents an algorithm for the solution of ac/dc power flow for the analysis of multi-terminal voltage source converter HVDC (VSC-MTDC) systems. This algorithm is described as sequential ac/dc power flow, which can be implemented easily in an existing ac power flow package and is very flexible when it compared with unified methods. The algorithm proposed in this paper is a general approach for analysis ac/dc power flows including loss-less VSC. The main contribution of this paper is development of sequential algorithm which uses the Gauss-Siedel algorithm to solve dc power balance equations, it offers two keys advantages: very fast and simple computational implementation. The algorithm presented in this paper is implemented on MATLAB[®] and

integrated into *Power System Toolbox* (PST).

The algorithm is tested using the WSCC 3-machine, 9-bus system with a 3-terminal MTDC network and results compared with those obtained from DIGSILENT[®] proposed algorithm. Results of the numerical simulation on this test network show the validity of the algorithm to include VSC MTDC system into an ac network. The author introduced a benchmark test system for the Phase I of Supergrid, the proposed algorithm is used to calculate dc power flows, results show the technical feasibility of heavy power flows interchanges between parties involved.

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